APPENDIX 4

SYNCHRONISATION BETWEEN UNBUNDLING AND PORTABILITY

Figure 3. Schematic representation of the processes of loop unbundling with portability (current situation pursuant to the portability regulation and the RUO) (with ε = 0 and ε = 2)
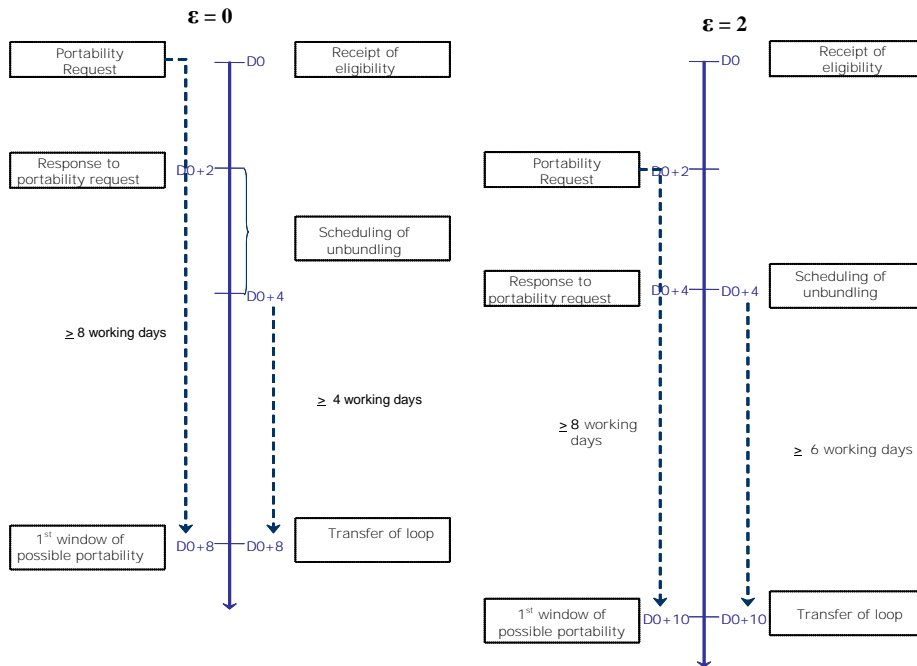

Figure 4. Schematic representation of the processes of loop unbundling with portability (amendments to portability regulation and proposed amendments to the RUO) (with ε = 0 and ε = 2)